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S/N 10/756961

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	SHIBATA et al.	Examiner:	Pham, H
Serial No.:	10/756961	Group Art Unit:	2814
Filed:	January 13, 2004	Docket No.:	10873.0871USD1
Title:	SEMICONDUCTOR MEMORY DEVICE WITH TRENCH-TYPE STACKED CELL CAPACITORS AND METHOD FOR MANUFACTURING THE SAME		

CERTIFICATE UNDER 37 CFR 1.6(d)

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on

October 5, 2005

By:

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Respectfully submitted,

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